

REMARKS

Claims 30, 43, 52, 68, 75 and 77 have been amended and claim 33 and 83 have been canceled. Claims 30-32, 34-82 and 84 are pending in the present application. Applicants reserve the right to pursue the original claims and other claims in this application and in other applications.

Claim 68 stands objected to based on informalities. The objection is respectfully traversed. Claim 68 has been amended in accordance with the Examiner's recommendations. Accordingly, the objection should be withdrawn and claim 68 allowed.

The drawings and the specification stand objected to as failing to show a storage node as part of a photodetector as claimed in claim 33. The objections are respectfully traversed. Claim 33 has been canceled. Accordingly, the objections should be withdrawn.

Claims 30, 33-38, 40, 42, 68-73, 75 and 76 stand rejected under 35 U.S.C. § 102(e) as being anticipated by Henderson. The rejection is respectfully traversed.

Claim 30 recites a pixel circuit comprising "an amplifier having an input coupled to said storage node and an output that provides an amplified input signal, said amplifier having a first power mode during a first operational period and a second power mode during a second operational period." Applicants respectfully submit that Henderson fails to disclose the claimed invention.

Although Henderson discloses an amplifier as part of a pixel circuit, the amplifier does not have two power modes. As such, the Henderson amplifier cannot have "a first power mode during a first operational period and a second power mode during a second operational period." Accordingly, Henderson fails to disclose the

claim 30 invention. Claims 34-38, 40 and 42 depend from claim 30 and are allowable along with claim 30 for at least the foregoing reasons. .

Claim 68 recites an integrated circuit comprising “an amplifier that amplifies a signal received from said storage node, said amplifier having a first power mode during a operational period and a second power mode during a second operational period.” As such, Applicants respectfully submit that claim 68 is allowable for at least the reasons set forth above and on its own merits. Claims 69-73 depend from claim 68 and are allowable along with claim 68 for at least the foregoing reasons.

Claim 75 recites a pixel sensor array comprising “an amplifier that amplifies a signal received from said photodetector, said amplifier having a first power mode during an integration period and a second power mode during a readout period.” As such, Applicants respectfully submit that claim 75 is allowable for at least the reasons set forth above and on its own merits. Claim 76 depends from claim 75 and is allowable along with claim 75 for at least the foregoing reasons.

Applicants respectfully submit that the rejection should be withdrawn and the claims allowed.

Claims 31-32, 39, 41, 62, 63, 74, 77, 78 and 80-84 stand rejected under 35 U.S.C. § 103(a) as being obvious over Henderson. The rejection is respectfully traversed.

Claims 31-32, 39 and 41 depend from claim 30 and are allowable along with claim 30 for at least the reasons set forth above and on their own merits.

Claims 62-63 depend from claim 52, which recites “an amplifier with an input that receives the first signal and an output that provides signals based on the first signal, said amplifier having a first power mode during a first operational period and a

second power mode during a second operational period.” As such, Applicants respectfully submit that claims 62-63 are allowable for at least the reasons set forth above and on their own merits. Claim 74 depends from claim 68 and is allowable along with claim 68 for at least the reasons set forth above and on its own merits.

Claim 77 recites an imaging system comprising “an amplifier that amplifies charges on said storage node, said amplifier having a first power mode during an integration period and a second power mode during a readout period.” As such, Applicants respectfully submit that claim 77 is allowable for at least the reasons set forth above and on its own merits. Claims 78, 80-82 and 84 depend from claim 77 and are allowable along with claim 77.

Applicants respectfully submit that the rejection should be withdrawn and the claims allowed.

Claims 41, 43-61, 64-67 and 79 stand rejected under 35 U.S.C. § 103(a) as being obvious over Henderson in view of Brehmer. The rejection is respectfully traversed.

Claim 41 depends from claim 30 and thus, recites a pixel circuit comprising “an amplifier having an input coupled to said storage node and an output that provides an amplified input signal, said amplifier having a first power mode during a first operational period and a second power mode during a second operational period.” As set forth above, Henderson fails to disclose the claim 30 invention. Applicants respectfully submit that Brehmer, which has been cited as teaching certain aspects of readout circuitry and distributed amplifiers, also fails to disclose, teach or suggest the claim 30 invention. As such, claim 30 is allowable over the cited combination. Accordingly, claim 41, which depends from claim 30 is allowable over the cited combination for at least the reasons set forth above and on its own merits.

Claim 43 recites an integrated circuit comprising “an amplifier with an input that receives the first signal and an output that provides an output signal based on the first signal, said amplifier having a first power mode during an integration period and a second power mode during a readout period.” Accordingly, Applicants respectfully submit that claim 43 is allowable over the cited combination for at least the reasons set forth above. Claims 44-51 depend from claim 43 and are allowable along with claim 43.

Claim 52 recites an integrated circuit comprising “an amplifier with an input that receives the first signal and an output that provides signals based on the first signal, said amplifier having a first power mode during a first operational period and a second power mode during a second operational period.” Accordingly, Applicants respectfully submit that claim 52 is allowable over the cited combination for at least the reasons set forth above. Claims 53-61 and 64-67 depend from claim 52 and are allowable along with claim 52.

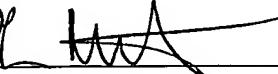
Claim 79 depends from claim 77 and is allowable along with claim 77.

Applicants respectfully submit that the rejection should be withdrawn and the claims allowed.

In view of the above amendment, Applicants believe the pending application is in condition for allowance.

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Respectfully submitted,

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